



Docket No.: M4065.0381/P381
(PATENT)

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of:
Leonard Forbes et al.

Allowed: November 20, 2003

Application No.: 09/808,114

Confirmation No.: 3126

Filed: March 15, 2001

Art Unit: 2826

For: TECHNIQUE TO MITIGATE SHORT
CHANNEL EFFECTS WITH VERTICAL
GATE TRANSISTOR WITH DIFFERENT
GATE MATERIALS

Examiner: K. Quinto

COMMENT ON REASONS FOR ALLOWANCE

Commissioner for Patents
Washington, DC 20231

Dear Sir:

In response to the Examiner's statement of reasons for allowance set forth in the Notice of Allowance mailed November 20, 2003, Applicants make the following comment. Applicants agree with the Examiner's reasons for allowance. However, Applicants believe that additional reasons for patentability exist as well based on limitations and combinations of limitations not commented upon by the Examiner, including limitations found in depending claims.

Dated: February 5, 2004

Respectfully submitted,

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